

Drafts Pending Active

- L1: (24) scan\$4 and ((emulat\$4 same core same (pro
- L2: (9179) idle near9 power
- L3: (83) idle near9 (configurat\$4 or command\$4)nea
- L4: (36743) programmable adj5 logic
- L5: (0) 12 and 13 and 14
- L6: (965) idle same (configurat\$4 or command\$4)sam
- L7: (6) 12 and 16 and 14
- L8: (1) 16 and 6553513.pn.
- L9: (1) 16 and 5300831.pn.

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 Highlight all the terms initially

12 and 16 and 14

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#	Document ID	Issue Date	Title	Current OR	C
1	<input type="checkbox"/> <input checked="" type="checkbox"/> US 20030106004 20030605		Method and apparatus for embedded built-in self-	714/733	3
2	<input type="checkbox"/> <input checked="" type="checkbox"/> US 6744274 B1 20040601		Programmable logic core adapter	326/16	3
3	<input checked="" type="checkbox"/> <input type="checkbox"/> US 6574333 B1 20030603		State machine based universal voice grade cards	379/377	3
4	<input type="checkbox"/> <input checked="" type="checkbox"/> US 6553513 B1 20030422		Emulation suspend mode with differing response t	714/28	7
5	<input type="checkbox"/> <input checked="" type="checkbox"/> US 6078593 A 20000620		Method and apparatus for reliable operation of u	370/498	3
6	<input type="checkbox"/> <input type="checkbox"/> US 5300831 A 19940405	19940405	Logic macro and protocol for reduced power consu	326/44	3

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## Active

- L1: (24) scan\$4 and ((emulat\$4 same core same (programmable adj5logic) same interfa\$4)
- L2: (9179) idle near9 power
- L3: (83) idle near9 (configurat\$4 or command\$4)near9 test\$4
- L4: (36743) programmable adj5 logic
- L5: (0) 12 and 13 and 14
- L6: (965) idle same (configurat\$4 or command\$4)same test\$4
- L7: (6) 12 and 16 and 14
- L8: (1) 16 and 6553513.pn.
- L9: (1) 16 and 5300831.pn.
- L10: (4063) programmable adj9 (core or adapt\$3)
- L11: (23942) test\$3 near9 interfac\$4
- L12: (46425) array near9(configurat\$4 or implement\$4)
- L13: (56540) array near9(configurat\$4 or implement\$4 or scal\$4)
- L14: (49580) register near5 transfer\$4
- L15: (9) 110 and 111 and 113 and 114
- L16: (20002) test\$3 near9 register
- L17: (7) 110 and 111 and 113 and 114 and 116
- L18: (6158) array same arithmetic
- L19: (2) 117 and 118

Document ID	Issue Date	Title	Current QE	QC
<input type="checkbox"/> <input checked="" type="checkbox"/> US 20010049763	20011206	Methods and apparatus for scalable array process	710/264	
<input checked="" type="checkbox"/> <input checked="" type="checkbox"/> US 6744274 B1	20040601 ✓	Programmable logic core adapter	326/16	3

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